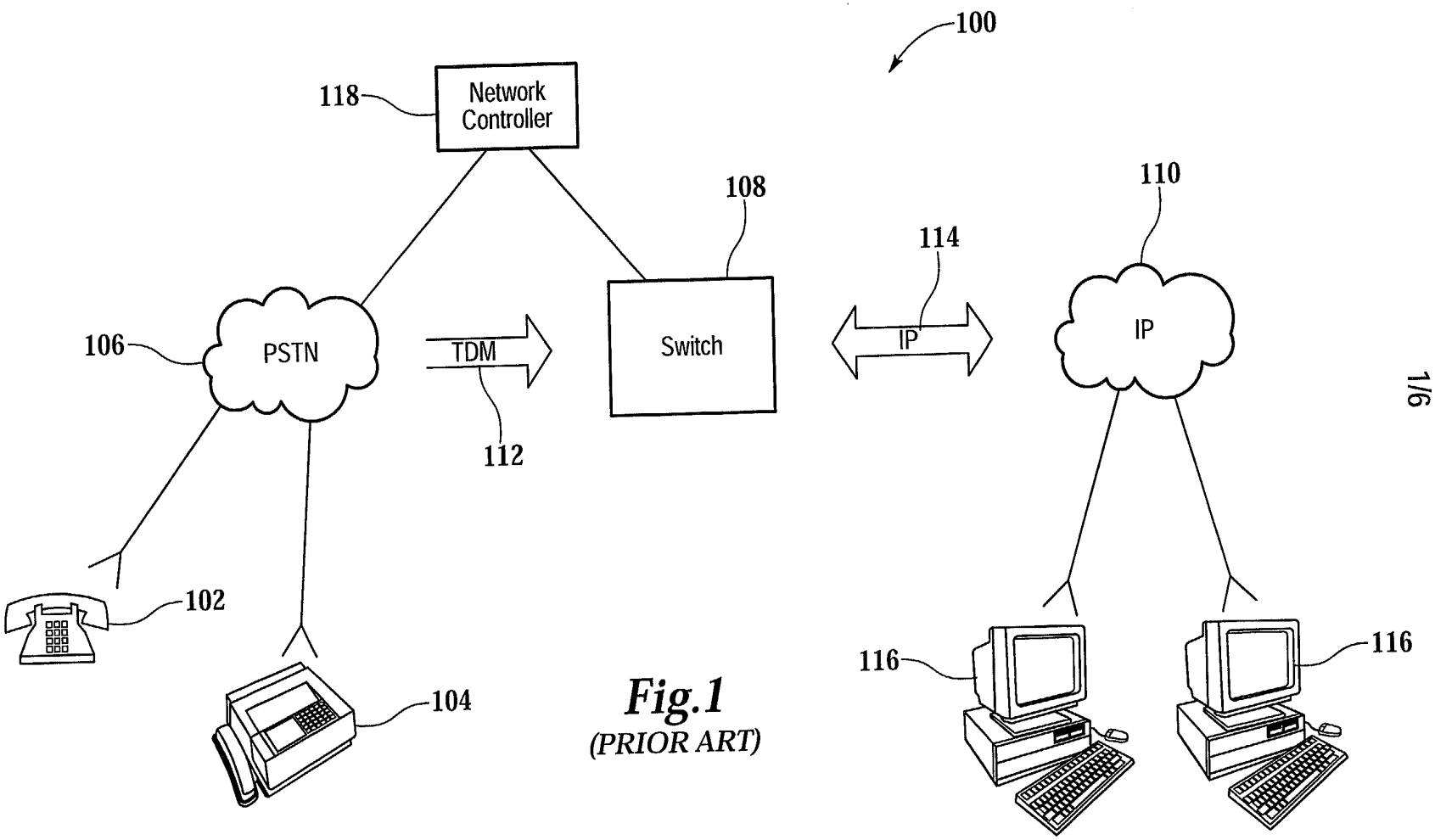

Fig.1
(PRIOR ART)

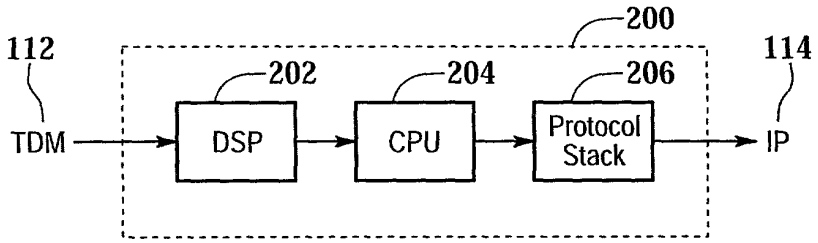

Fig.2A
(PRIOR ART)

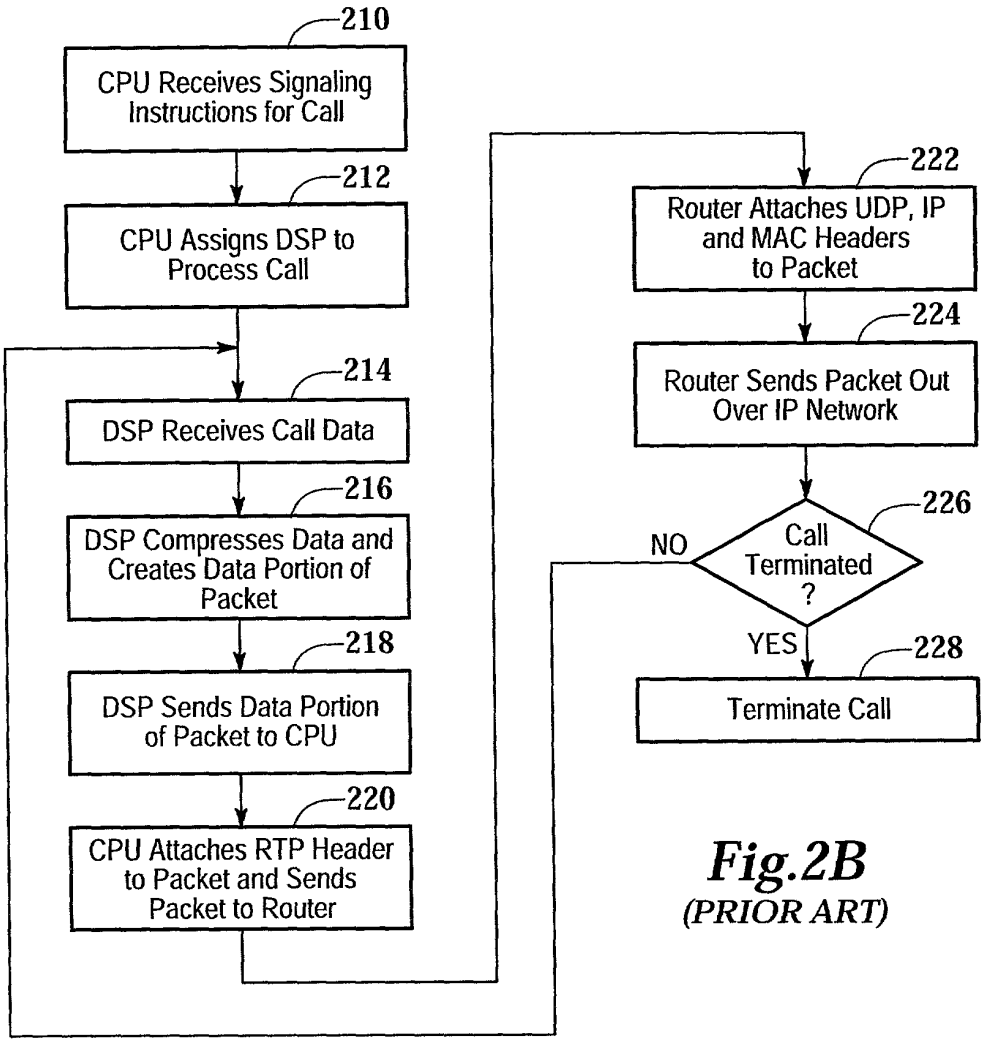

Fig.2B
(PRIOR ART)

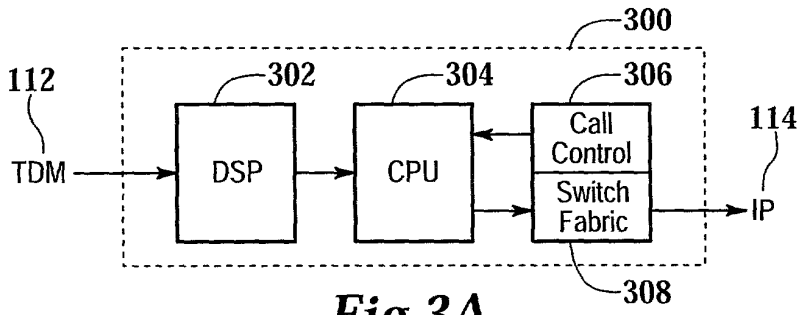

Fig.3A
(PRIOR ART)

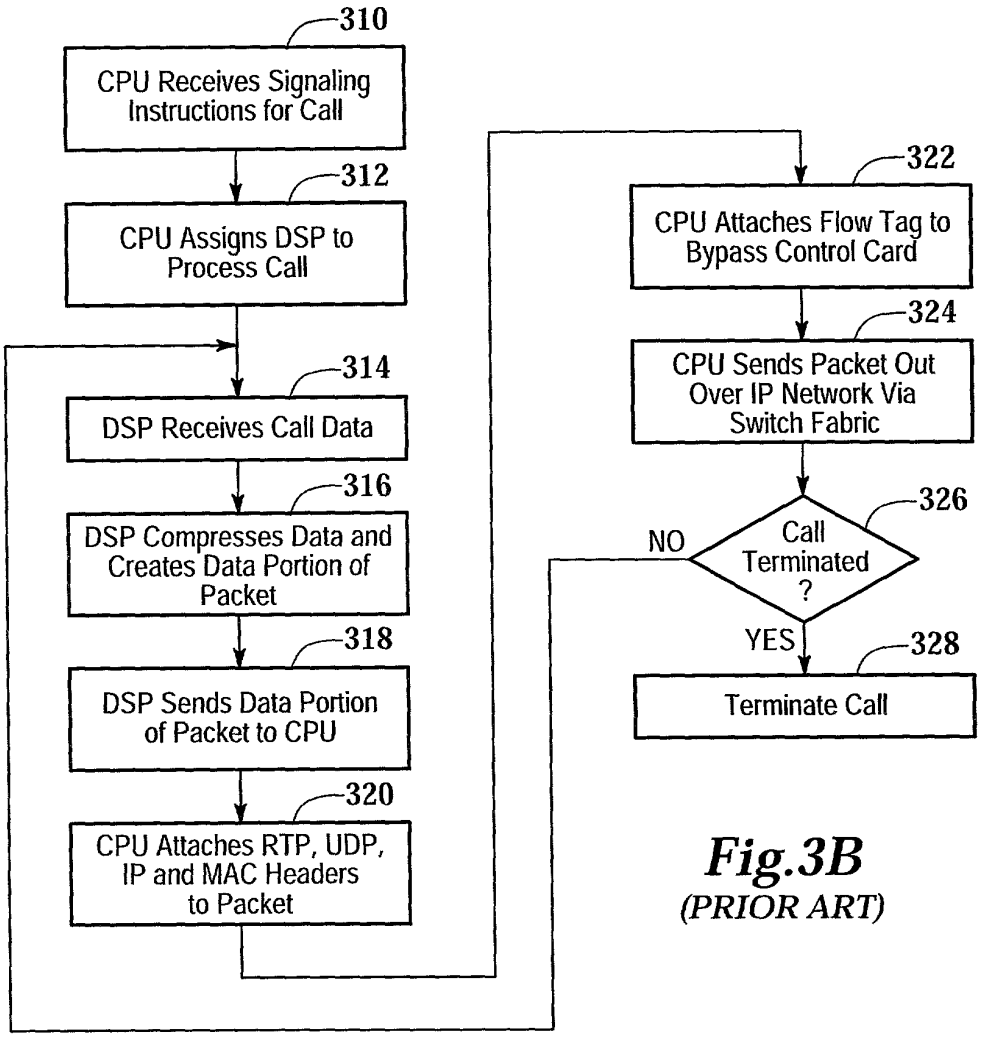

Fig.3B
(PRIOR ART)

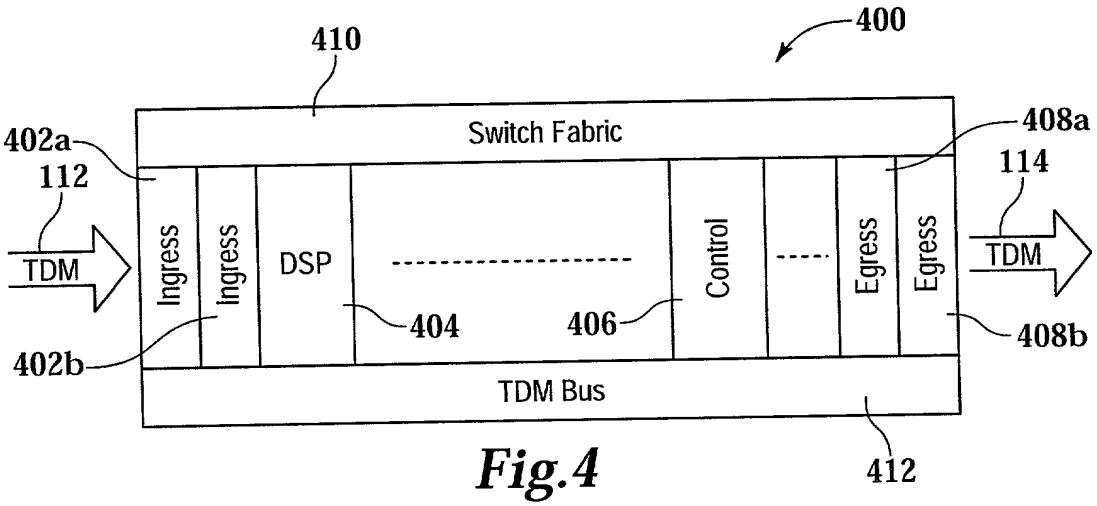
400
410
Switch Fabric
402a
112
408a
114
TDM
Ingress
Ingress
DSP
Control
Egress
Egress
TDM
402b
404
406
408b
TDM Bus
412

Fig.4

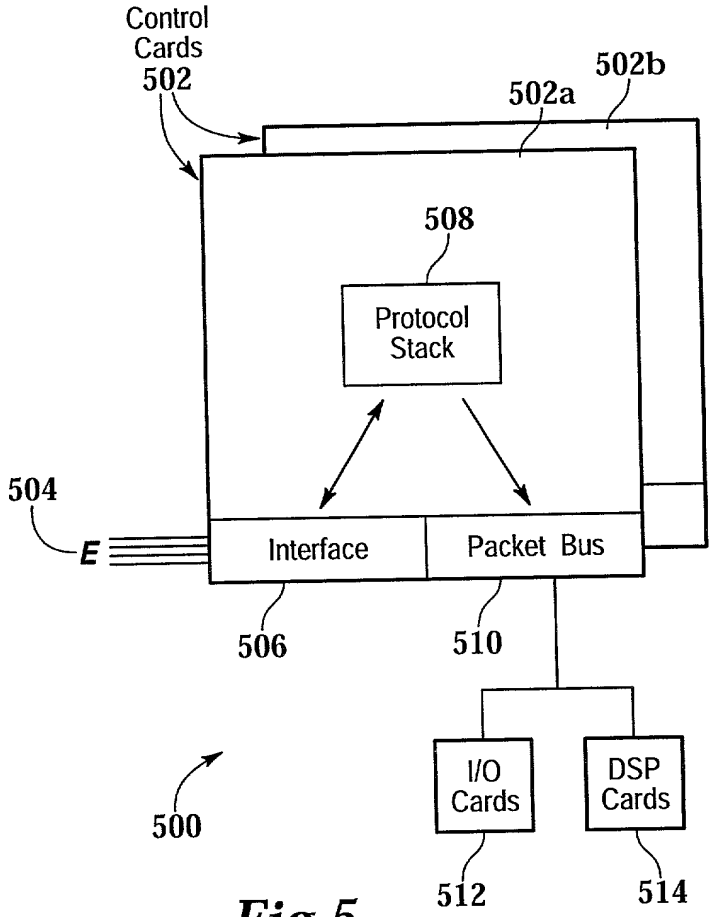
Control
Cards
502
502b
502a
508
Protocol
Stack
504
E
Interface
Packet Bus
506
510
I/O
Cards
DSP
Cards
500
512
514

Fig.5

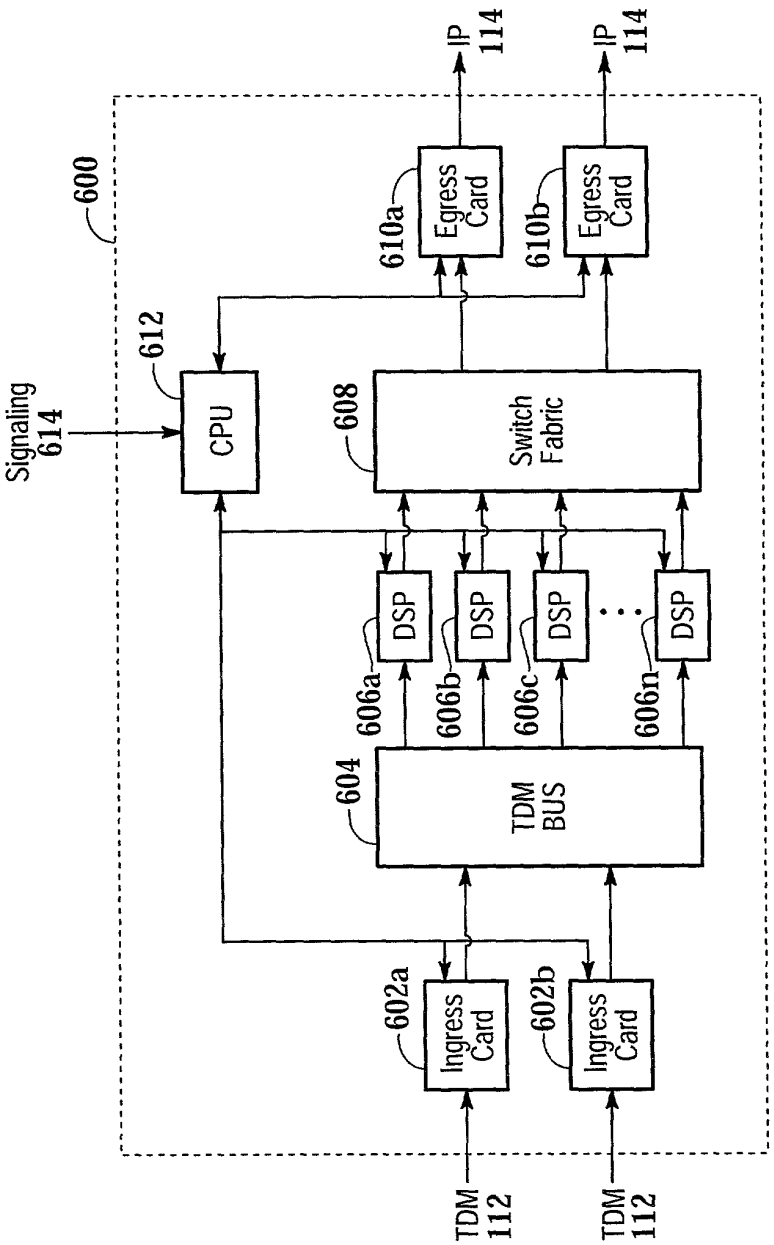
600
612
CPU
Signaling
614
608
Switch
Fabric
610a
Egress
Card
610b
Egress
Card
IP
114
IP
114
DSP
DSP
DSP
DSP
606a
606b
606c
606n
604
TDM
BUS
602a
Ingress
Card
602b
Ingress
Card
TDM
112
TDM
112

Fig.6A

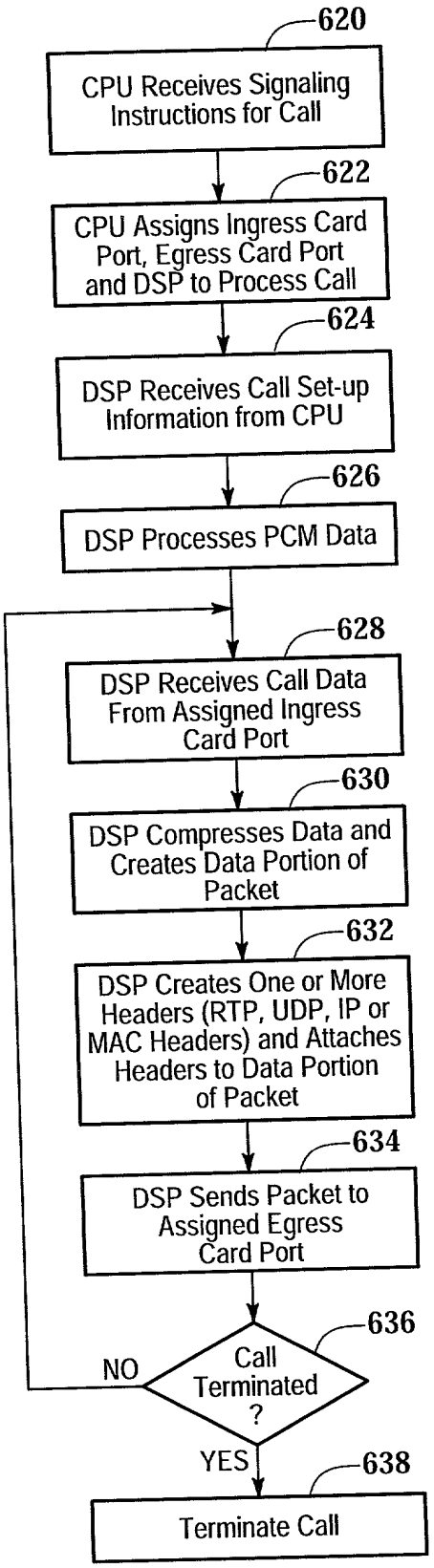
620
CPU Receives Signaling Instructions for Call
622
CPU Assigns Ingress Card Port, Egress Card Port and DSP to Process Call
624
DSP Receives Call Set-up Information from CPU
626
DSP Processes PCM Data
628
DSP Receives Call Data From Assigned Ingress Card Port
630
DSP Compresses Data and Creates Data Portion of Packet
632
DSP Creates One or More Headers (RTP, UDP, IP or MAC Headers) and Attaches Headers to Data Portion of Packet
634
DSP Sends Packet to Assigned Egress Card Port
636
Call Terminated ?
NO
YES
638
Terminate Call

Fig.6B